MOSFETs Silicon N-Channel MOS (π-MOSVII)

# TK12A65D

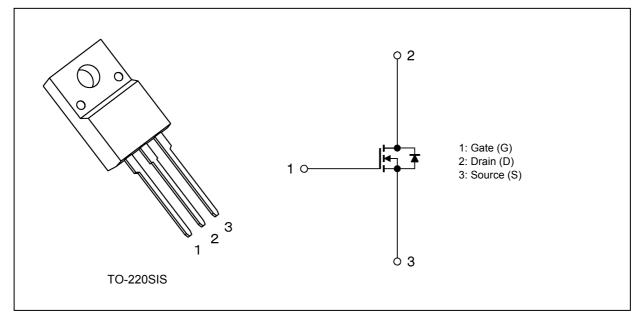
## 1. Applications

Switching Voltage Regulators

### 2. Features

- (1) Low drain-source on-resistance:  $R_{DS(ON)} = 0.46 \Omega$  (typ.)
- (2) High forward transfer admittance:  $|\,Y_{\rm fs}\,|$  = 6.0 S (typ.)
- (3) Low leakage current:  $I_{DSS} = 10 \ \mu A \ (max) \ (V_{DS} = 650 \ V)$
- (4) Enhancement mode:  $V_{th}$  = 2.0 to 4.0 V (V\_{DS} = 10 V,  $I_{D}$  = 1 mA)

## 3. Packaging and Internal Circuit



## 4. Absolute Maximum Ratings (Note) (T<sub>a</sub> = 25°C unless otherwise specified)

Characteristics	Symbol	Rating	Unit	
Drain-source voltage		V <sub>DSS</sub>	650	V
Gate-source voltage		V <sub>GSS</sub>	±30	
Drain current (DC)	(Note 1)	Ι <sub>D</sub>	12	А
Drain current (pulsed)	(Note 1)	I <sub>DP</sub>	48	
Power dissipation (T	<sub>c</sub> = 25°C)	PD	50	W
Single-pulse avalanche energy	(Note 2)	E <sub>AS</sub>	611	mJ
Avalanche current		I <sub>AR</sub>	12	А
Repetitive avalanche energy	(Note 3)	E <sub>AR</sub>	5.0	mJ
Channel temperature		T <sub>ch</sub>	150	°C
Storage temperature		T <sub>stg</sub>	-55 to 150	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

### 5. Thermal Characteristics

Characteristics	Symbol	Max	Unit
Channel-to-case thermal resistance	R <sub>th(ch-c)</sub>	2.5	°C/W
Channel-to-ambient thermal resistance	R <sub>th(ch-a)</sub>	62.5	

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2:  $V_{DD}$  = 90 V,  $T_{ch}$  = 25°C (initial), L = 7.5 mH,  $R_G$  = 25  $\Omega$ ,  $I_{AR}$  = 12 A

Note 3: Repetitive rating; pulse width limited by maximum channel temperature

Note: This transistor is sensitive to electrostatic discharge and should be handled with care.

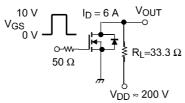
### 6. Electrical Characteristics

## 6.1. Static Characteristics (Ta = 25°C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current	I <sub>GSS</sub>	$V_{GS}$ = ±30 V, $V_{DS}$ = 0 V	_	_	±1	μA
Drain cut-off current	I <sub>DSS</sub>	V <sub>DS</sub> = 650 V, V <sub>GS</sub> = 0 V	_	_	10	
Drain-source breakdown voltage	V <sub>(BR)DSS</sub>	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	650	_	—	V
Gate threshold voltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	2.0	—	4.0	
Drain-source on-resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 6 A	_	0.46	0.54	Ω
Forward transfer admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 6 A	1.7	6.0	_	S

## 6.2. Dynamic Characteristics ( $T_a = 25^{\circ}C$ unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Input capacitance	C <sub>iss</sub>	V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 0 V, f = 1 MHz	_	2300	_	pF
Reverse transfer capacitance	C <sub>rss</sub>			10	_	
Output capacitance	C <sub>oss</sub>			200	_	
Switching time (rise time)	t <sub>r</sub>	See Figure 6.2.1.		35	_	ns
Switching time (turn-on time)	t <sub>on</sub>			90	_	
Switching time (fall time)	t <sub>f</sub>			20	_	
Switching time (turn-off time)	t <sub>off</sub>			150	_	



Duty  $\leq$  1%,  $t_W =$  10  $\mu s$ 



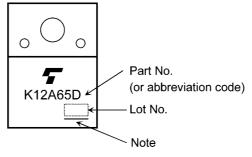
## 6.3. Gate Charge Characteristics ( $T_a = 25^{\circ}C$ unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Total gate charge (gate-source plus gate-drain)	Qg	$V_{DD} \approx 400 \text{ V},  V_{GS} \text{ = } 10  \text{V},  \text{I}_{D} \text{ = } 12 \text{ A}$	—	40	—	nC
Gate-source charge	Q <sub>gs</sub>		_	28	_	
Gate-drain charge	Q <sub>gd</sub>		_	12	_	

## 6.4. Source-Drain Characteristics ( $T_a = 25^{\circ}C$ unless otherwise specified)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Reverse drain current (DC)	(Note 1)	I <sub>DR</sub>	—	_	_	12	А
Reverse drain current (pulsed)	(Note 1)	I <sub>DRP</sub>	—			48	
Diode forward voltage		V <sub>DSF</sub>	I <sub>DR1</sub> = 12 A, V <sub>GS</sub> = 0 V			-1.7	V
Reverse recovery time		t <sub>rr</sub>	I <sub>DR</sub> = 12 A, V <sub>GS</sub> = 0 V	_	1400	_	ns
Reverse recovery charge		Q <sub>rr</sub>	-dI <sub>DR</sub> /dt = 100 A/μs		16		μC

## 7. Marking (Note)



#### Fig. 7.1 Marking

 Note:
 A line under a Lot No. identifies the indication of product Labels.

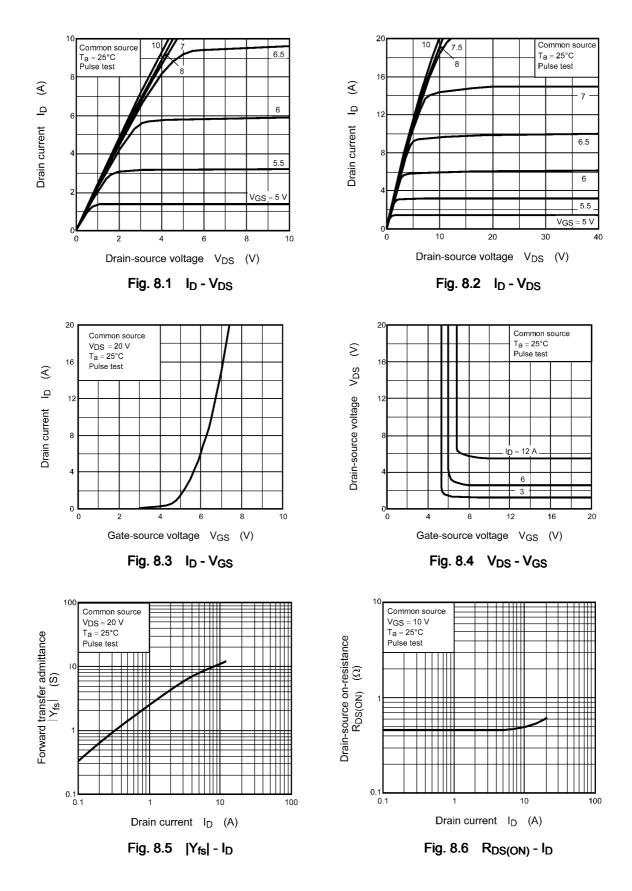
 Not underlined: [[Pb]]/INCLUDES > MCV

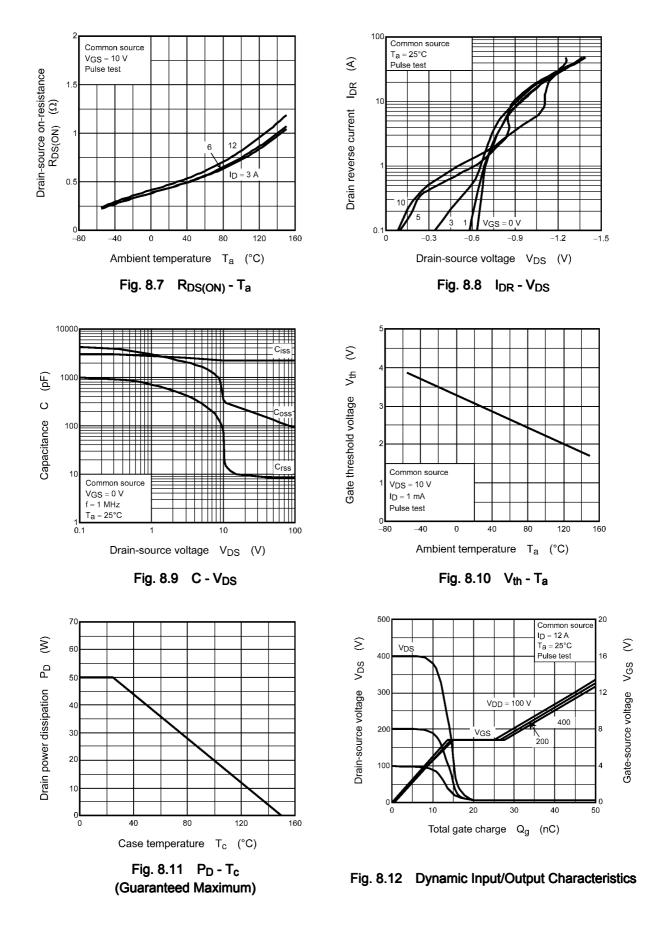
 Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

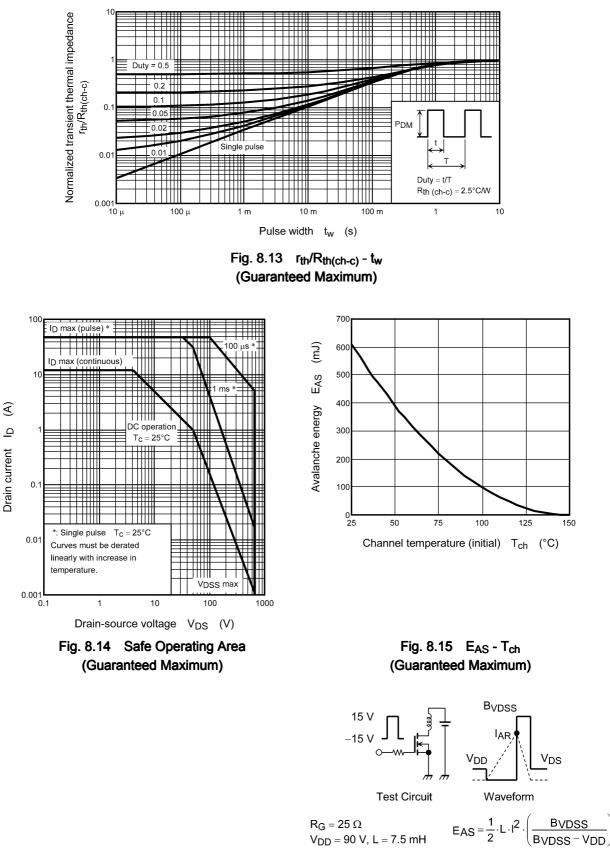
 Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product.

 The RoHS is the Directive 2002/95/EC of the European Parliament and of the Council of 27 January 2003 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

## 8. Characteristics Curves (Note)







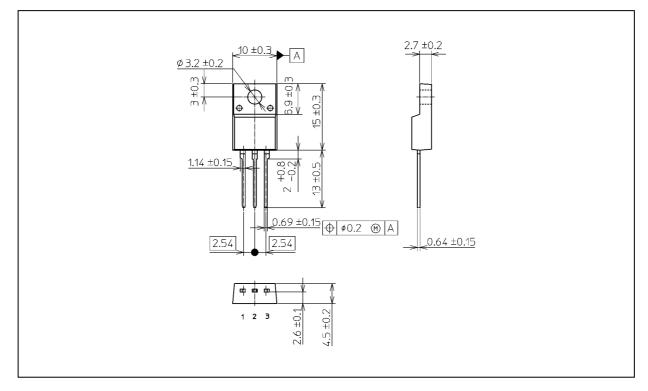


Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

## TK12A65D

### Package Dimensions

Unit: mm



#### Weight: 1.7 g (typ.)

Package Name(s)				
JEITA: SC-67				
TOSHIBA: 2-10U1S				
Nickname: TO-220SIS				

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